



Patent
UC1.PAU.05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**IN RE APPLICATION OF:
YIN TAT MA**

SERIAL NO. : 10/501,651

FILED: JAN. 10, 2005

**FOR: ON-CHIP ESD PROTECTION
CIRCUIT FOR COMPOUND
SEMICONDUCTOR
HETEROJUNCTION BIPOLAR
TRANSISTOR RF CIRCUITS**

Examiner: D.H. Patel

Group Art Unit: 2836

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

In reply to the Office Action mailed Feb. 9, 2006, please enter the following amendments and consider the following remarks.

Amendments to the Drawings begin on page 2.

Amendments to the Claims begin on page 3.

Remarks begin on page 11.

Substitute Sheets of the Drawings are attached as Appendix 1.